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Applicants:	Prashant Sethi, et al.	§ Group Art Unit:	2181
Serial No.:	10/806,787	§	
Filed:	March 22, 2004	§ Examiner:	Chun Kuan Lee
For:	A Method And System For Configuration Of Processor Integrated Devices In Multi-Processor Systems	§ Atty. Dkt. No.:	ITL.1576US (P18870)

POWER OF ATTORNEY BY ASSIGNEE

Under the provisions of 37 C.F.R. § 3.71, the undersigned assignee of record of the entire interest in the above-identified patent/patent application by virtue of an assignment recorded (check as applicable):

Concurrently Herewith
Date Recorded August 9, 2004
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elects to conduct the prosecution of the application/maintenance of the patent to the exclusion of the Inventor(s). The undersigned hereby declares that he has reviewed the above-referenced assignment and hereby declares that, to the best of his knowledge, title is in the Assignee, and further declares that all statements made herein of his own knowledge are true and that all statements made on information and belief are believed to be true. The assignee hereby revokes any previous powers of attorney and appoints the practitioners associated with:

Customer No. 47795

to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

The undersigned is authorized to sign this statement on behalf of the Assignee.

Please direct all communications to: **Customer No. 21906**

Please direct all telephone calls to: Mark J. Rozman at (512) 418-9944.

ASSIGNEE
Intel Corporation

Date: 2/26/07

BY: Pam Matlock

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